

Appl. No.: 09/401,616
Amdt. dated July 28, 2003
Reply to Office action of March 28, 2003

Amendments to the Specification

On page 16-17, please amend the paragraph beginning at page 16, line 27, as follows:

A¹
"In an alternate embodiment, the first interrupt handler stores data that identifies the destination of each instruction that can cause transfer of control flow. Such instructions include conditional branches, unconditional branches, jumps, subroutine calls and subroutine returns. In general, destinations are identified by their memory addresses. For conditional branches, the destinations may alternatively be identified by a single taken/not-taken bit. A complete path profile can be stored as a sequence of the destination information for each interpreted control flow instruction using encoding. One encoding technique includes the first interpreted program counter value with a first in-order list storing the taken/not-taken bits for each conditional branch instruction, and with a second in-order list storing the destination addresses for other control flow instructions. In an alternate embodiment, the size of the path ~~information~~ information is reduced by storing hash values of the destination addresses rather than the full addresses. In another alternate embodiment, the size of the path information is reduced using a compression technique."

On page 27, please amend the paragraph beginning at page 27, line 1, as follows:

A²
"In an alternate embodiment, the optimizer 59 is separate from the compiler and operates directly on ~~compiled~~ compiled binaries without source code."

On page 43, please amend the first paragraph as follows:

A³
"WHAT IS CLAIMED IS:
~~Interrupt driven value sampling~~"